File Edit Chart Filter Window Tools Help						an ar 1 an an an an an an an an ar 186 an 1	<u>File Display Plugins Help</u>				
	8 -3 -9 T6 III t A I= 7 @ º	Timeline			Alle A. B. Alle A. B. A. A. Alle A.		Absolute	- Absolute	- Absolute		· Sy
0 s	50 s 1	00 s 150 s	200 s 250 s		ses, Accumulated Exclusive Time pe		Metric tree	Call tree	System tree Statistics	5	ste
Master thread:0 OMP Target [0:1]:0 Master thread:1 OMP Target [1:1]:1 Master thread:2 OMP Target [1:1]:1 Master thread:3 OMP Target [3:1]:3 Master thread:4 OMP Target [4:1]:4 Master thread:5 OMP Target [5:1]:5 Master thread:6 GPU Events over Time hine Cray generic r onde nid005002 ice gfx90a (0) ice gfx90a (2) ice gfx90a (2) ice gfx90a (3) ice gfx90a (5) ice gfx90a (6) ice gfx90a (7) ice gfx90a (7) ice gfx90a (7)				Masteead:0 OMP T1]:0 Masteead:1 OMP T1]:0 Masteead:2 OMP T1]:1 Masteead:3 OMP T1]:3 Masteead:4 OMP T1]:3 Masteead:4 OMP T1]:4 Masteead:5 OMP T1]:4 Masteead:6 Accumulated Exx 1,000 s 1,354.787 s 1,298.834 s 1,241.426 s 1,191.076 s 516.456 s Stomp target (sumt @sx0027558)	S 50 s 100 s 150 s Somp72556b Isomp74b40 12 somp7556b Isomp74b40 12 somp	200 s 250 s Others Others Others Others Others Others Others Others Others Others Others Others **	 I.81e8 Visits (occ) 7673.64 Time (sec) 0.00 Minimum Inclusive Time (sec) 0.00 Minimum Inclusive Time (sec) 0 bytes_get (bytes) 0 io_bytes_retad (bytes) 0 io_bytes_written (bytes) 2.37e+11 bytes_sent (bytes) 1.61e9 OpenMP Memory (bytes) 5.43e+11 dalocation_size (bytes) 0 bytes_leaked (bytes) 1.61e9 maximum_heap_memory_allocated (bytes) 	 26.83 tealeaf base.clang_omp_target_gfx90a (1 hidden child) 1.09 main (8 hidden children) 20.55 initialise_comms 33.37 initialise_application 4.32 diffuse_overload (31 hidden children) 0.27 t\$cmp target @0x14d4876 (callee id=362, target tyr) 1.75 solve (4 hidden children) 0.48 cg driver 1.06 cg init_driver 1.06 cg init_driver 1.40 cg calc_w 1.49 cg calc_w 1.4727.2 t\$cmp target @0x00274b40 (callee id=39 143.52 zum_over_ranks 	 - machine Cray generi - onde nido05002 - openMP Device g - openMP Device g - 74.56 OpenMP Device 74.56 OpenMP Device 74.26 OpenMP Device 74.32 OpenMP Device 0.00 MPI Rank 0 0 0.00 MPI Rank 1 0.00 MPI Rank 2 	c fx90a (0) streams et [031] fice gfx90a (1) fice gfx90a (2) fice gfx90a (2) fice gfx90a (4) fice gfx90a (5) fice gfx90a (5) fice gfx90a (5) fice gfx90a (6) fice gfx90a (1) fice gfx90a (1) fice gfx90a (2) fice gfx90a (3) fice gfx90a (3) fice gfx90a (3) fice gfx90a (4) fice gfx90a (6) fice gfx90a (6)	em View Topologies General
- node nid005003			Master thread:1	!\$omp target submit @0x0027556b	•1e •1\$omp target submit @0x00274b	140 ee		4.31 !somp target (kernel execution) @0x0027d345	0.00 MPI Rank 8		,
•ice gfx90a (0) •ice gfx90a (1)			OMP Target [1:1]:1	Somp target (kernel execution) @0x0027556b	\$ somp target (kernel execution)	@0x00274b40		 0.34 !\$omp target (kernel execution) @0x00273294 (ciller id 1191.08 !\$omp target (kernel execution) @0x00274b40 (call 	er id=2		
•ice gfx90a (2) •ice gfx90a (3)			Master thread:2	!somp target submit @0x0027556b	•• !\$omp target submit @0x00274b	40		 1298.83 !\$omp target (kernel execution) @0x0027556b (call 482.22 !\$omp target (kernel execution) @0x00275fd2 (caller 0.27 !\$omp target (kernel execution) @0x0028b0e7 (caller in the target (kernel execution) @0x0028b0e7 (caller in the target (kernel execution) @0x0028b0e7 (caller in the target kernel execution) @0x0028b0e7 (caller in the target kernel execution) @0x0028b0e7 (caller in target kernel execution) @0x0028b0e7	id=268 0.00 MPI Rank 13		
•ice gfx90a (4)			OMP Target [2:1]:2	\$ somp target (kernel execution) @0x0027	556b (\$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$) @0x00274b40		 13 20 Isomp target (kernel execution) @0x0028b0e7 (caller id 13 20 Isomp target (kernel execution) @0x0027ef85 	All (48 elements)		- V
•ice gfx90a (5) •ice gfx90a (6)			• Master thread:3	!\$omp target submit @0x0027556b	• • !\$omp target submit @0x00274b	40	0.00 7673.64 (100.00%) 7673.6	4 0.00 1191.08 (15.52%)		37 (6.29%) 1191.0	.08
•ice gfx90a (7)			OMP Target [3:1]:3	\$000275	6b !\$omp target (kernel execution)		Ready				

27th POP Webinar

Performance Analysis of OpenMP Target Offloading in Score-P

2024-05-28 | Jan André Reuter



Mitglied der Helmholtz-Gemeinschaft

AGENDA

- What is the Score-P infrastructure?
- The OpenMP Tools Interface and our support of it
- OpenMP and offloading
- Handling offloading events on the host
- Handling offloading events from accelerators
- Results
- What if my runtime has only limited support?
- Final words

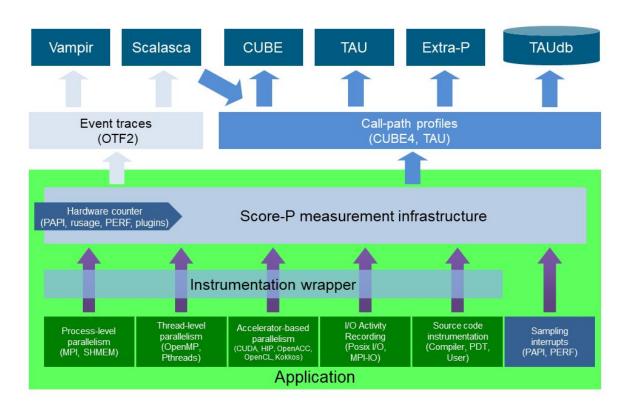


WHAT IS SCORE-P?

The Score-P instrumentation and measurement infrastructure

- Score-P is a highly scalable instrumentation tool
- Support for multi-process, thread-parallel and accelerator-based paradigms
- Support for additional metrics (I/O, HW counters, ...)
- Flexible measurement without re-compilation:
 - Profile generation (CUBE4 .cubex format)
 - Event trace recording (OTF2 format)
- Support for C, C++, Fortran and Python







INSTRUMENTING APPLICATIONS

A very high level overview on how to use Score-P

//Sources/OpenMP/hello-world scorep-nvc -mp=multicore hello-world.c //Sources/OpenMP/hello-world OMP_NUM_THREADS=4 ./a.out Hello World from thread 0 Hello World from thread 2 Hello World from thread 3 //Sources/OpenMP/hello-world scorep-score -r scorep-*/profile.cubex

Estimated aggregate size of event trace: 790 bytes Estimated requirements for largest trace buffer (max_buf): 790 bytes Estimated memory requirements (SCOREP_TOTAL_MEMORY): 11MB (hint: When tracing set SCOREP_TOTAL_MEMORY=11MB to avoid intermediate flushes or reduce requirements using USR regions filters.)

flt	type	max_buf[B]	visits	time[s]	time[%]	time/visit[us]	region
	ALL	789	22	0.00	100.0	47.58	ALL
	OMP	628	16	0.00	45.4	29.68	OMP
	COM	120	5	0.00	28.8	60.20	COM
	SCOREP	41	1	0.00	25.9	270.87	SCOREP
	OMP	340	4	0.00	33.8	88.49	!\$omp parallel @hello-world.c:14
	OMP	96	4	0.00	2.2	5.80	<pre>!\$omp implicit barrier @hello-world.c:14</pre>
	COM	96	4	0.00	0.7	1.81	hello_world
	OMP	96	4	0.00	6.6	17.31	!\$omp critical @hello-world.c:6
	OMP	96	4	0.00	2.7	7.14	<pre>!\$omp critical sblock @hello-world.c:6</pre>
	SCOREP	41	1	0.00	25.9	270.87	a.out
	COM	24	1	0.00	28.1	293.74	main
		~/Sour	rces/Ope	enMP/hell	Lo-world	🕨 # Create filt	er if needed

ces/OpenMP/hello-w

Hello World from thread 0 Hello World from thread 1 Hello World from thread 3 Hello World from thread 2



Build systems like CMake may need additional steps. See scorep-wrapper --help for more info.

More information



Using POP Tools: Score-P, Scalasca Bernd Mohr



Hello World from thread 2

./a.out

OMP NUM THREADS=4 \

SCOREP_ENABLE_TRACING=true \

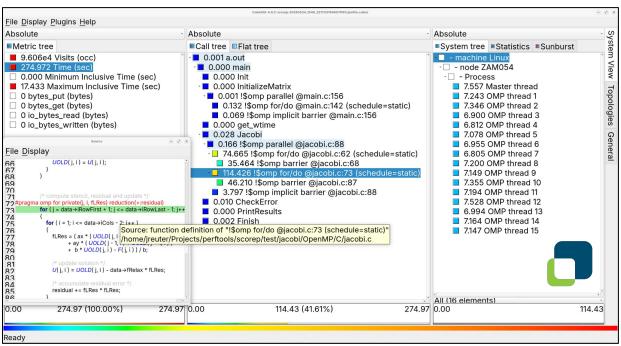
SCOREP TOTAL MEMORY=11MB \

SCOREP_FILTERING_FILE=initial_scorep.filter \

Slide 4

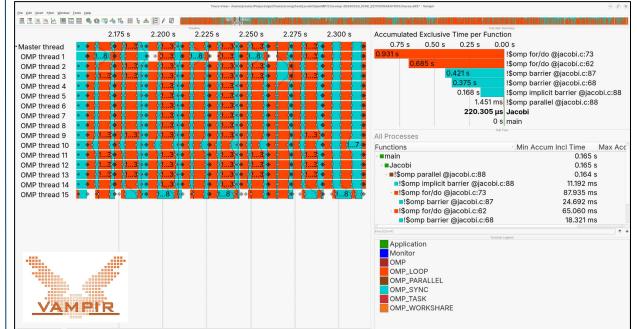
VIEW YOUR RESULTS

A short look at Cube and Vampir



- Open source viewer for . cubex profiles
- More information:

https://www.scalasca.org/scalasca/software/cube-4.x/



- Commercial viewer for .otf2 traces
- More information:

https://vampir.eu



rnre-P

Scalable performance measurem

infrastructure for parallel codes

Slide 5

OPENMP SUPPORT IN SCORE-P

Two ways to collect information about OpenMP



- Source-to-source instrumentation tool
- Independent from compiler used
- Instrumentation up to OpenMP 3.x
- Various limitations
 - Code sometimes has to be prepared for OPARI2





OPENMP SUPPORT IN SCORE-P

Two ways to collect information about OpenMP



- Source-to-source instrumentation tool
- Independent from compiler used
- Instrumentation up to OpenMP 3.x
- Various limitations
 - Code sometimes has to be prepared for OPARI2





- Standardised tool interface since OpenMP 5.0
- Enables development of tools using any implementation of the OpenMP API
- Support for the latest and greatest
 OpenMP features
- Continuously expanded with new

versions



Slide 7

WHAT IS THE OPENMP TOOLS INTERFACE?



• Interface for *first-party* tools, linked or loaded into the OpenMP program

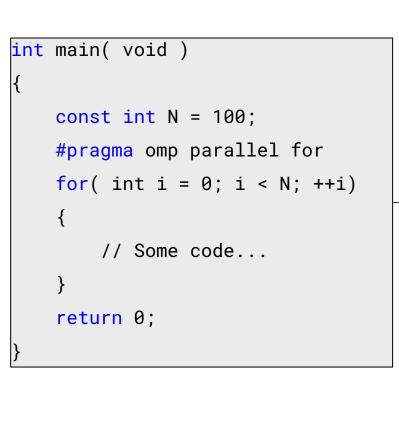
```
$ clang -fopenmp my-program.c -lmy-tool
$ clang -fopenmp my-program.c my-tool.c
$ OMP_TOOL_LIBRARIES=my-tool.so ./my-program
```

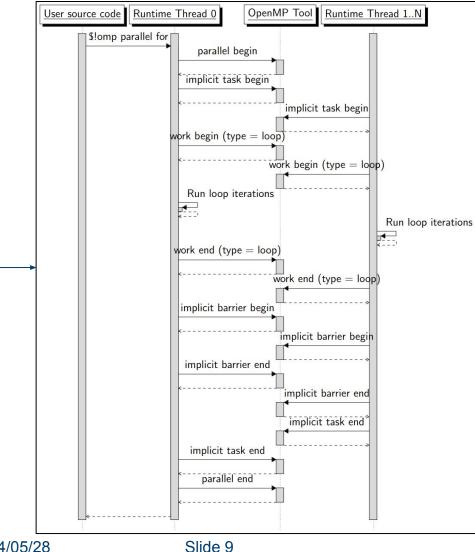
- Defined by the OpenMP standard, implemented by OpenMP runtimes
- Tools have to implement functions to interact with OMPT (e.g. ompt_start_tool)
- One interaction method: *callbacks*, invoked for runtime events



OPENMP TOOLS INTERFACE CALLBACKS

Just a part of what tools see for user code







- Tools receive a lot of events for user code
- With this, we are able to record events for OpenMP directives



SCORE-P AND OMPT: THE PRESENT ...



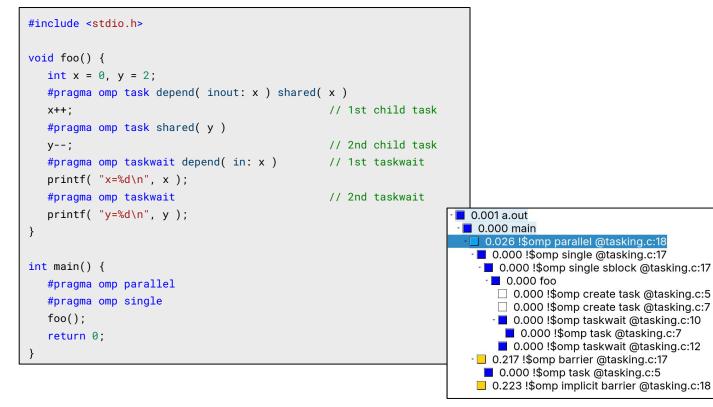
- First support in version 8.0 (released in December 2022)
- Selectable via scorep --thread=omp:ompt
- Support tried to match the available features in OPARI2 (focused on OpenMP 3.x)
- Small feature additions and several bug fixes in 8.x:
 - Recording loop schedules
 - Recording omp_test_lock events

OpenMP directive	Support
OpenMP 3.x	yes
cancel	no
task depend	no
task detach	no
taskgroup	yes
taskloop	no
teams	no
scope	no





- Current OpenMP features planned for Score-P v9.0
 - Improvements to support of task directives

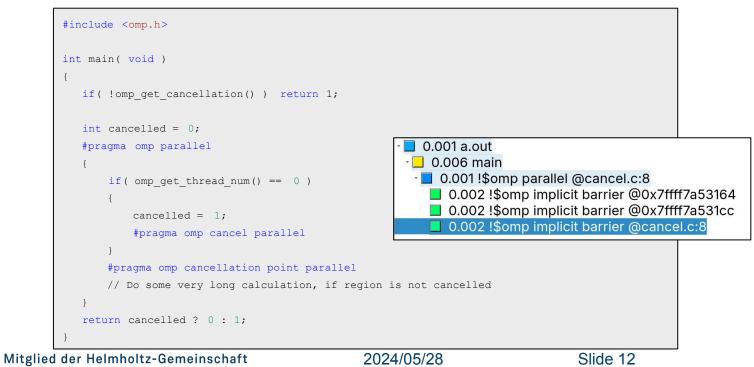


OpenMP directive	Support
OpenMP 3.x	yes
cancel	partial
task depend	partial
task detach	yes
taskgroup	yes
taskloop	yes
teams	yes
scope	no





- Current OpenMP features planned for Score-P v9.0
 - Improvements to support of task directives
 - Improved support for cancel directive



OpenMP directive	Support
OpenMP 3.x	yes
cancel	partial
task depend	partial
task detach	yes
taskgroup	yes
taskloop	yes
teams	yes
scope	no



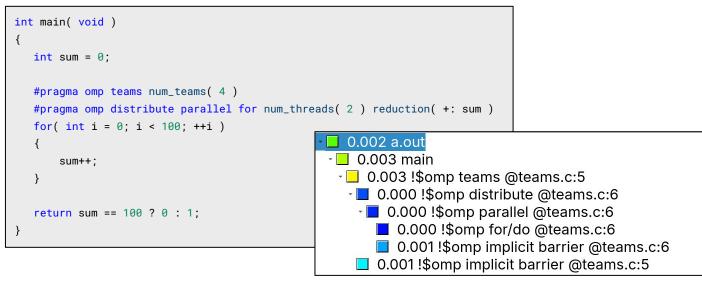


Support

yes

partial

- Current OpenMP features planned for Score-P v9.0
 - Improvements to support of task directives
 - Improved support for cancel directive
 - Support for teams directive



		portion
	task depend	partial
	task detach	yes
	taskgroup	yes
	taskloop	yes
	teams	yes
	scope	no
1		

OpenMP directive

OpenMP 3.x

cancel





- Current OpenMP features planned for Score-P v9.0
 - Improvements to support of task directives
 - Improved support for cancel directive
 - Support for teams directive
 - Recording of reduction clause

PROGRAM REDUCTION3 N = 0	
N = 0 !\$OMP PARALLEL DO REDUCTION(+: N)	- 🗖 0.001 a.out
	0.003 MAIN_
DO I = 1, 100	
N = N + I	0.006 !\$omp parallel @reduction.f90:3
END DO	0.000 !\$omp for/do @reduction.f90:0
	0.000 !\$omp reduction @reduction.f90:0
WRITE (*,*) N	0.000 !\$omp reduction @reduction.f90:3
END PROGRAM REDUCTION3	0.002 !\$omp implicit barrier @reduction.f90:3

OpenMP directive	Support
OpenMP 3.x	yes
cancel	partial
task depend	partial
task detach	yes
taskgroup	yes
taskloop	yes
teams	yes
scope	no



COMPILER SUPPORT FOR OMPT

We do have strict requirements

- OpenMP runtimes have to implement the tools interface
- However, runtimes may have bugs or features not fully implemented
- We test the OpenMP runtime during configuration to prepare for known issues



Compiler	Host	Events	Accelerator Events	
Compiler	Host	Target	Device Tracing	
AOMP 18.0-1	Full	Full	Full	
CCE 17.0.0	None	Full	None	
Clang 18.1.6	Full	Partial	None	
GCC 14.1	None	None	None	
NVHPC 24.5	Full	Full	None	
oneAPI 2024.1	Full	Partial	None	
ROCm 6.1	Full	Full	Partial	

OMPT support:	yes	OMPT support:	yes	OMPT support:	no
OMPT header:	yes	OMPT header:	yes	OMPT header:	yes
OMPT tool:	yes	OMPT tool:	yes	OMPT tool:	yes
OMPT C support:	yes	OMPT C support:	yes	OMPT C support:	
OMPT C++ support:	yes	OMPT C++ support:	yes	OMPT C++ support:	
OMPT Fortran support:	yes	OMPT Fortran support:	yes	OMPT Fortran support:	
OMPT critical checks p	assed: \	OMPT critical checks p	bassed: \		
	yes		yes	CCE	16.0.1
OMPT remediable checks	; passed: \	OMPT remediable checks			
	yes			test_lock_mutex, missing_wor	<pre>rk_loop_schedule detected</pre>
OMPT is default:	yes	OMPT is default:	yes		
NVHPC 24	4.5	oneAPI 20)24.1		
Mitglied der Helmholtz-Ge	emeinschaft	2024/05	5/28	Slide 15	



RIGOROUS TESTING



Ensuring that Score-P is able to work with compiler runtimes

- Large internal test suite, based on OpenMP examples and additional smoke tests
- Score-P is regularly tested with GCC, LLVM/Clang, NVHPC, ROCm & oneAPI
- Allows easy testing of new compilers as soon as they release
 - NVHPC 24.5, released on May 22nd, did show differences in runtime for example
- Contributed to several bug reports to compiler vendors (~65 bugs since Dec. 2022) and workarounds in Score-P

	С	C++	Fortran
Host Examples	138	7	165
Offload Examples	63	6	63
Teams Smoke Tests	30	0	0
General Smoke Tests	42	4	0



HOW TO HANDLE OFFLOADING TO ACCELERATORS?

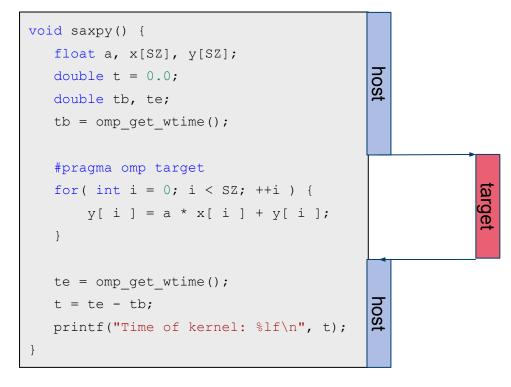


OFFLOADING WORK TO ACCELERATORS



What is offloading in OpenMP?

- OpenMP introduced target directives in OpenMP 4.0
- Expanded in later spec. versions, including functions like omp_target_alloc



More information:





OFFLOADING WORK TO ACCELERATORS



Splitting data transfers and kernels

```
void saxpy( float a, float* x, float* y, int n ) {
  #pragma omp target teams distribute parallel for
  for( int i = 0; i < n; ++i ) {</pre>
      y[i] = a * x[i] + y[i];
void vecadd( float* x, float* y, float* data out, int n )
  #pragma omp target teams distribute parallel for
  for ( int i = 0; i < n; ++i ) {</pre>
      data out[ i ] = x[ i ] + y[ i ];
void example() {
   float a, x[N], y[N], b[N], data out[N];
  #pragma omp target data map ( to: x[:N], y[:N], b[:N], a ) \
                           map( from: data out[:N] )
      saxpy( a, x, y, N );
      vecadd( y, b, data out, N );
```

Simple optimizations:

- Reduce numbers of data transfers
- Reuse data, if possible
- Transfer data beforehand, maybe asynchronously

More information:

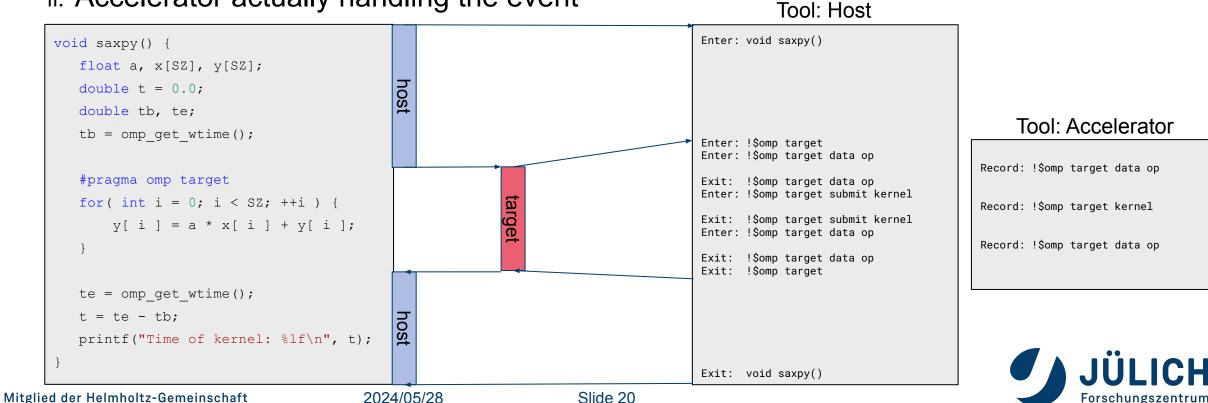




ACCELERATOR EVENTS AND TOOLS



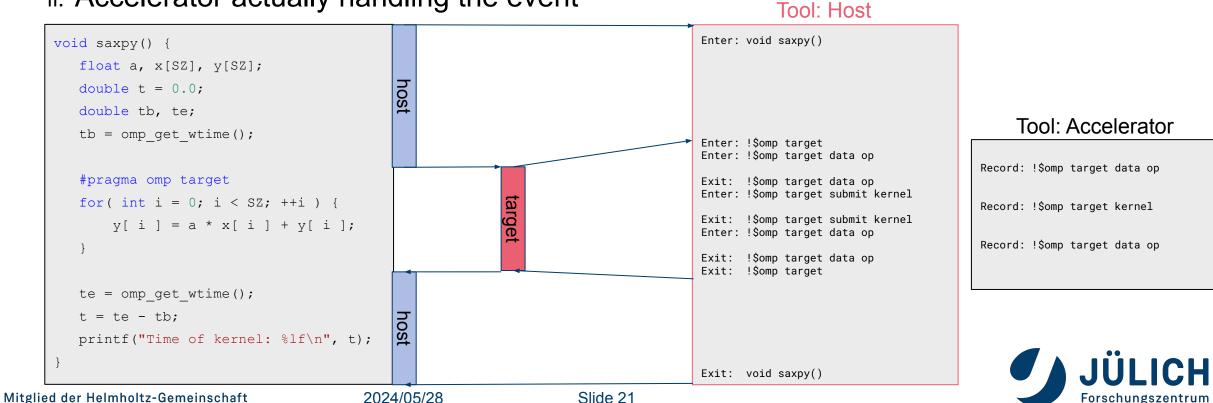
- Unlike host directives, offloading needs to be handled twice to get all information
 - i. Host side dispatching events and waiting for completion
 - ii. Accelerator actually handling the event



ACCELERATOR EVENTS AND TOOLS

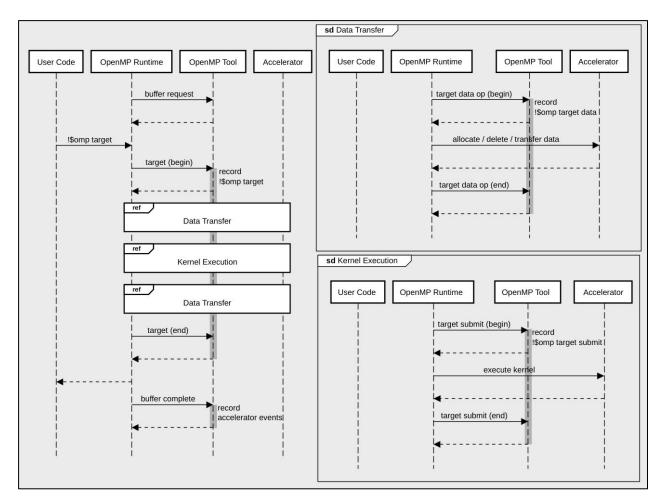


- Unlike host directives, offloading needs to be handled twice to get all information
 - i. Host side dispatching events and waiting for completion
 - ii. Accelerator actually handling the event





- Host side events are similar to existing ones (e.g. parallel_begin)
- We receive one begin and end event for the following scenarios:
 - target directives
 - data transfers
 - submit of a kernel
- This includes directives and function calls





Getting more into the details



<pre>typedef void (*ompt_callback_target_emi_t) (</pre>						
ompt_target_t kind,						
ompt_scope_endpoint_t endpoint,						
int device_num,						
ompt_data_t* task_data,						
ompt_data_t* target_task_data,						
ompt_data_t* target_data,						
const void* codeptr_ra);						

typedef void (*ompt_callback_submit_emi) (
 ompt_scope_endpoint_t endpoint,
 ompt_data_t* target_data,
 ompt_id_t* host_op_id,
 unsigned int requested_num_teams);

• Three callbacks, giving us the important information

<pre>typedef void (*ompt_callback_target_data_op_emi) (ompt_scope_endpoint_t endpoint, ompt_data_t* target_task_data, ompt_data_t* target_data, ompt_id_t* host_op_id, ompt_target_data_op_t optype, void* src_addr, int src_device_num, void* dest_addr, int dest_device_num, size_t bytes, somet_wridt* endpoint_target_data_not_one, int src_device_num, size_t bytes, somet_unidt* endpoint_target_data_not_one, int src_device_num, size_t bytes, somet_unidt* endpoint_target_data_not_one, int src_device_num, int src_device_num,</pre>	
const void* codeptr_ra);	



Getting more into the details



typedef void (*ompt_callba	ack_target_emi_t) (
ompt_target_t	kind,
<pre>ompt_scope_endpoint_t</pre>	endpoint,
int	device_num,
ompt_data_t*	task_data,
ompt_data_t*	target_task_data,
ompt_data_t*	target_data,
const void*	codeptr_ra);

typedef void (*ompt_callback_submit_emi) (
 ompt_scope_endpoint_t endpoint,
 ompt_data_t* target_data,
 ompt_id_t* host_op_id,
 unsigned int requested_num_teams);

cypedel ferd (empe_edites	
ompt_scope_endpoint_t	endpoint,
ompt_data_t*	target_task_data,
ompt_data_t*	target_data,
ompt_id_t*	host_op_id,
ompt_target_data_op_t	optype,
void*	src_addr,
int	<pre>src_device_num,</pre>
void*	dest_addr,
int	dest_device_num,
size_t	bytes,
const void*	codeptr_ra);

typedef void (*ompt callback target data op emi) (

- Three callbacks, giving us the important information
- Start / end of the operation



Getting more into the details



typedef void (*ompt_callb	ack_target_emi_t) (
ompt_target_t	kind,
ompt_scope_endpoint_t	endpoint,
int	device_num,
ompt_data_t*	task_data,
ompt_data_t*	target_task_data,
ompt_data_t*	target_data,
const void*	codeptr_ra);

typedef void (*ompt_callback_submit_emi) (
 ompt_scope_endpoint_t endpoint,
 ompt_data_t* target_data,
 ompt_id_t* host_op_id,
 unsigned int requested_num_teams);

typedef void (*ompt_callb ompt_scope_endpoint_t	endpoint,
ompt_data_t*	target_task_data,
ompt_data_t*	target_data,
ompt_id_t*	host_op_id,
<pre>ompt_target_data_op_t</pre>	optype,
void*	src_addr,
int	<pre>src_device_num,</pre>
void*	dest_addr,
int	dest_device_num,
size_t	bytes,
const void*	<pre>codeptr_ra);</pre>

- Three callbacks, giving us the important information
- Start / end of the operation
- Information about what exactly is done



Getting more into the details



typedef void (*ompt_callb	ack_target_emi_t) (
ompt_target_t	kind,
ompt_scope_endpoint_t	endpoint,
int	device_num,
ompt_data_t*	task_data,
ompt_data_t*	target_task_data,
ompt_data_t*	target_data,
const void*	<pre>codeptr_ra);</pre>

typedef void (*ompt_callback_submit_emi) (
 ompt_scope_endpoint_t endpoint,
 ompt_data_t* target_data,
 ompt_id_t* host_op_id,
 unsigned int requested_num_teams);

typedef void (*ompt_callba ompt_scope_endpoint_t	
ompt_data_t*	target_task_data,
ompt_data_t*	target_data,
ompt_id_t*	host_op_id,
ompt_target_data_op_t	• •
void*	src_addr,
int	<pre>src_device_num,</pre>
void*	dest_addr,
int	dest_device_num,
size_t	bytes,
const void*	<pre>codeptr_ra);</pre>

- Three callbacks, giving us the important information
- Start / end of the operation
- Information about what exactly is done
- Source code position



Getting more into the details



typedef void (*ompt_callb	ack_target_emi_t) (
ompt_target_t	kind,
ompt_scope_endpoint_t	endpoint,
int	device_num,
ompt_data_t*	task_data,
ompt_data_t*	target_task_data,
ompt_data_t*	target_data,
const void*	<pre>codeptr_ra);</pre>

typedef void (*ompt_callback_submit_emi) (
 ompt_scope_endpoint_t endpoint,
 ompt_data_t* target_data,
 ompt_id_t* host_op_id,
 unsigned int requested_num_teams);

l	<pre>typedef void (*ompt_callback_target_data_op_emi) (</pre>				
L	<pre>ompt_scope_endpoint_t endpoint,</pre>				
L	ompt_data_t* target_task_data,				
L	ompt_data_t* target_data,				
L	ompt_id_t* host_op_id,				
	ompt_target_data_op_t optype,				
	void* src_addr,				
	int src_device_num,				
	void* dest_addr,				
	int dest_device_num,				
	size_t bytes,				
	<pre>const void* codeptr_ra);</pre>				

- Three callbacks, giving us the important information
- Start / end of the operation
- Information about what exactly is done
- Source code position
- Unique information per target region and operation



CORRELATION OF EVENTS



- We do need to correlate two things:
 - Host callbacks between each other
 - Host callbacks to accelerator events
- Done via target_data and host_op_id
- What needs to be transferred?

typedef void (*ompt_callba ompt_scope_endpoint_t	
ompt_data_t*	target_data,
ompt_id_t*	host_op_id,
unsigned int	<pre>requested_num_teams);</pre>



CORRELATION OF EVENTS



- We do need to correlate two things:
 - Host callbacks between each other
 - Host callbacks to accelerator events
- Done via target_data and host_op_id
- What needs to be transferred?

```
typedef struct scorep_ompt_target_data_t {
    const void* codeptr_ra;
    ompt_id_t target_id;
    bool supports_device_tracing;
} scorep_ompt_target_data;
```

typedef void (*ompt_callba ompt_scope_endpoint_t	
ompt_data_t*	target_data
<pre>ompt_id_t* unsigned int</pre>	<pre>host_op_id, requested_num_teams);</pre>



CORRELATION OF EVENTS



- We do need to correlate two things:
 - Host callbacks between each other
 - Host callbacks to accelerator events
- Done via target_data and host_op_id
- What needs to be transferred?

```
typedef struct scorep_ompt_target_data_t {
    const void* codeptr_ra;
    ompt_id_t target_id;
    bool supports_device_tracing;
} scorep_ompt_target_data;
```

		following		-		•••	•		
* *	0000000 hostLoc	0 00000000 ationId	00000000	 	00000000 hostOpId	00000000	00000000	00000000	00000000
* *,									

typedef void (*ompt_callback_submit_emi) (
ompt_scope_endpoint_t	endpoint,		
ompt_data_t*	target_data,		
ompt_id_t*	host_op_id,		
unsigned int	<pre>requested_num_teams);</pre>		

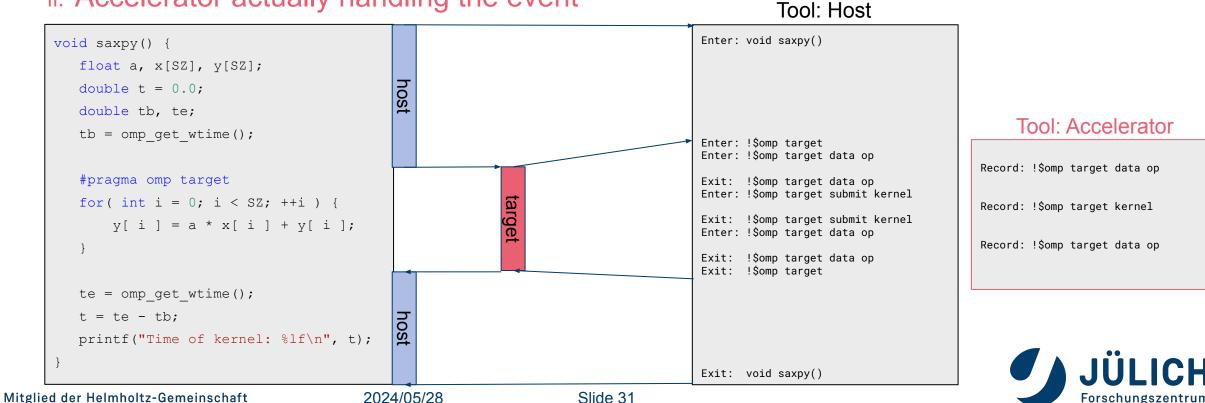


ACCELERATOR EVENTS AND TOOLS



- Unlike host directives, offloading needs to be handled twice to get all information
 - i. Host side dispatching events and waiting for completion





THE DEVICE TRACING INTERFACE

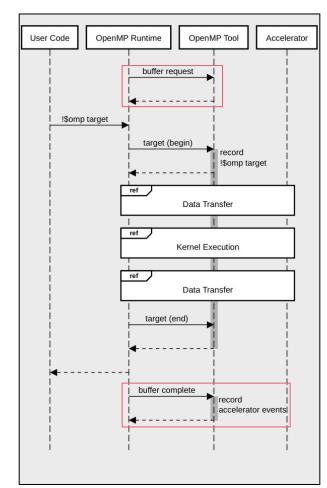
What is it?

- Buffer-based handling of accelerator events (similar to CUPTI, rocTracer)
- When a device is initialized, we can enable this interface
- Runtime will ask for buffers, record events, flush full buffers

What tools need to do:

- Sort buffers, as runtimes are not required to sort buffer
- Convert timestamps, either manually or via ompt_translate_time
- Iterate through buffer and write events





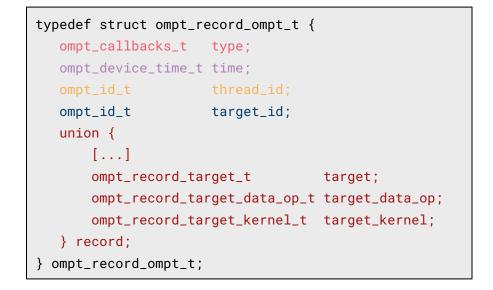


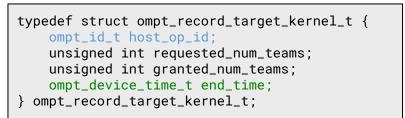
THE DEVICE TRACING INTERFACE

What does a record contain?

- Each record contains:
 - What type of event is recorded
 - When the record was recorded
 - Which thread recorded the record
 - The mapped target_data of the tool
 - The actual record of the callback
- Actual records may contain more information, like end timestamp and our set host_op_id

2024/05/28



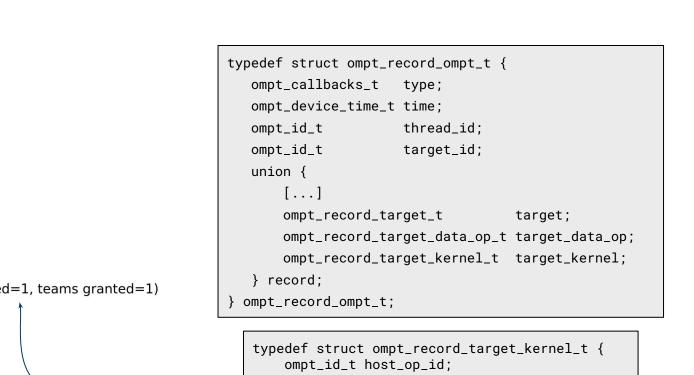


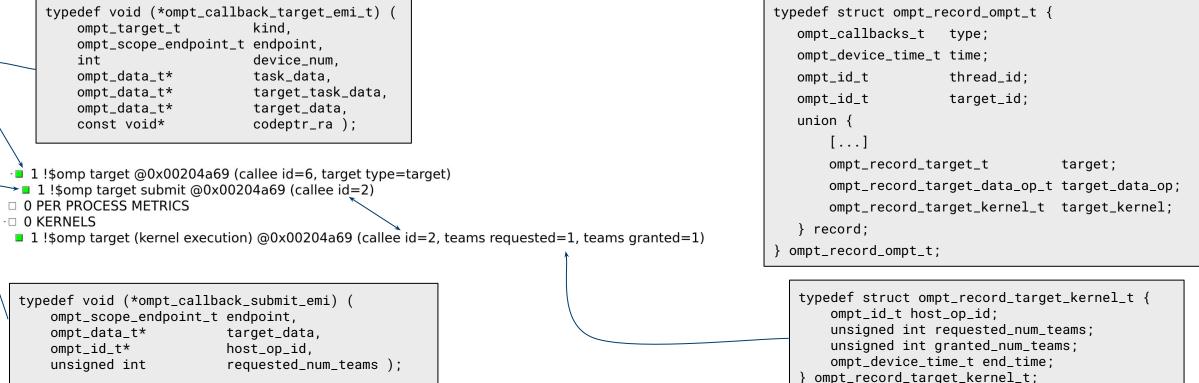




BRINGING BOTH TOGETHER

What can we record with callbacks and the device tracing interface?







infrastructure for parallel codes

HOW DO I USE THE NEW FEATURES?

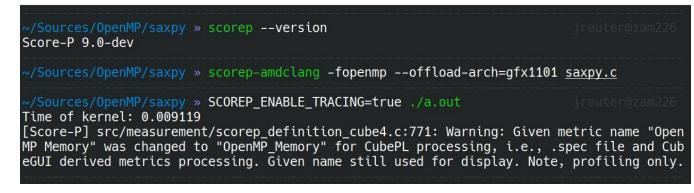


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HOW TO USE THE NEW FEATURES?

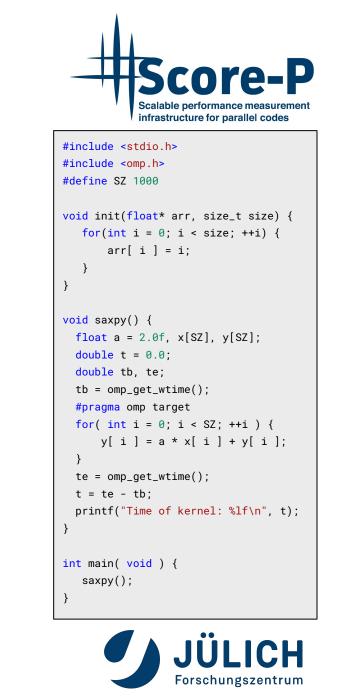
There are no additional steps needed!

- The OpenMP Tools Interface will be the default with Score-P v9.0
- Example:

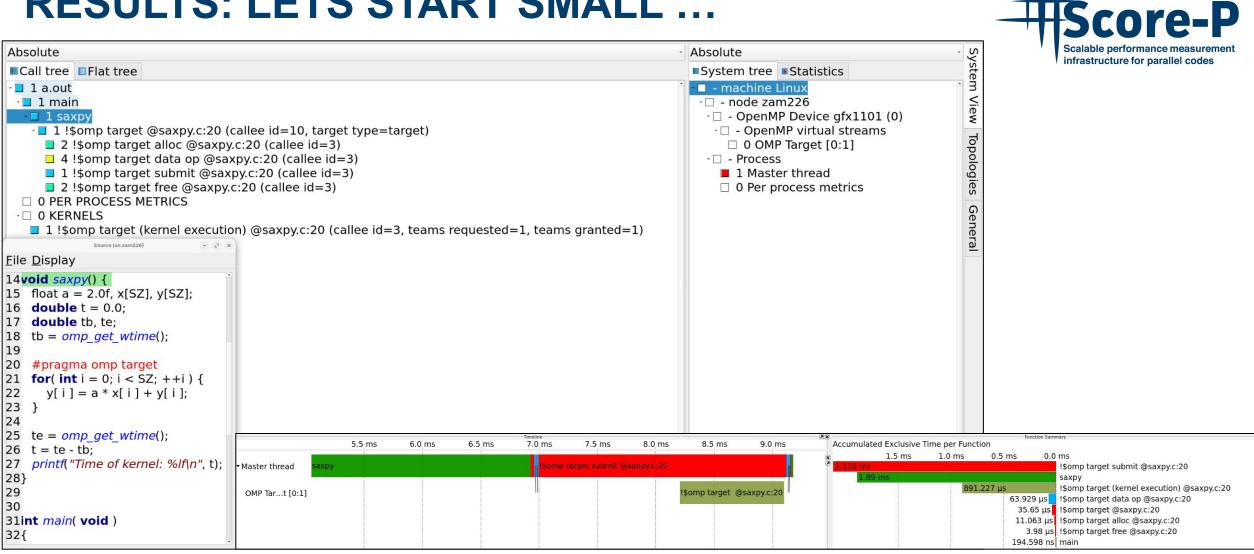


• If device tracing cannot be activated, a message is shown

[Score-P] src/adapters/ompt/scorep_ompt_events_device_tracing.inc.c:85: Warning: Device Tracing interface could not be initial ized and will be disabled for device NVIDIA GeForce MX550 (0). This will lead to accelerator events not showing up in the resu lts. Lookup function is NULL. Time of kernel: 0.074238

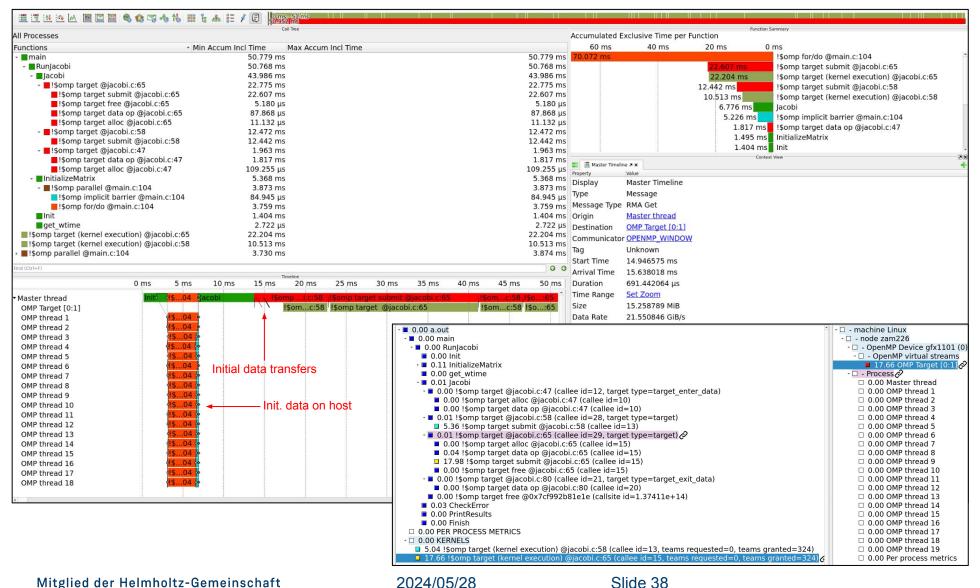


RESULTS: LETS START SMALL ...





INCREASE THE COMPLEXITY ...





- Jacobi example • used in Score-P testing
- System info:
 - Ubuntu 22.04 0
 - ROCm 6.1.0 0
 - RX 7700 XT



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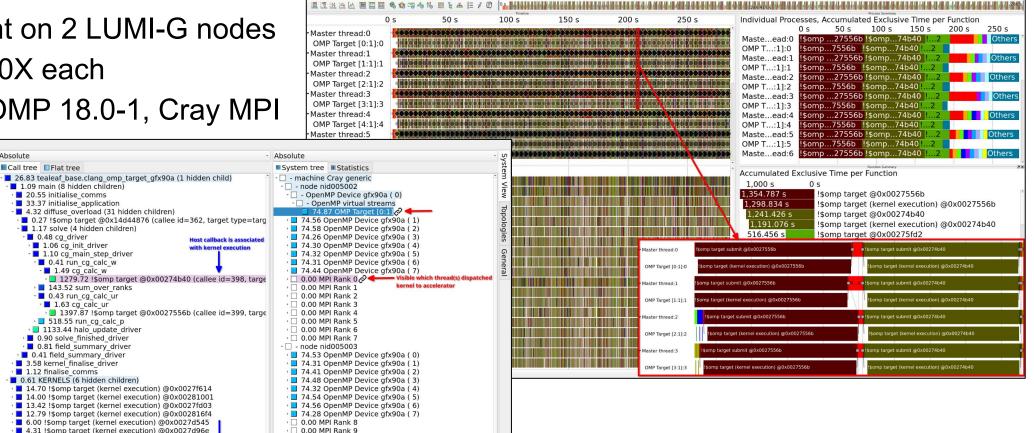
... AND SCALE IT UP!



Running the SPEC HPC tealeaf benchmark on multiple LUMI-G nodes

- Measurement on 2 LUMI-G nodes with 4x MI250X each
- Compiler: AOMP 18.0-1, Cray MPI

Absolute







7673.64 0.00

7673.64 (100.00%)

Eile Display Plugins Help

1.81e8 Visits (occ)

0 bytes put (bytes)

0 bytes_get (bytes)

0 io bytes read (bytes)

0 bytes leaked (bytes)

0 io_bytes_written (bytes)

2.37e+11 bytes sent (bytes)

2.37e+11 bytes_received (bytes)

1.61e9 OpenMP Memory (bytes)

5.43e+11 allocation size (bytes)

5.43e+11 deallocation size (bytes)

1.61e9 maximum heap memory allocated (bytes)

0.00 Minimum Inclusive Time (sec)

285.92 Maximum Inclusive Time (sec)

7673.64 Time (se

Absolute

0.00

Metric tree

0.34 !\$omp target (kernel execution) @0x00273294 (Coller id=202)

1298.83 !\$omp target (kernel execution) @0x0027556b (caller id=2

482.22 !somp target (kernel execution) @0x00275fd2 (caller id=268

0.27 !\$omp target (kernel execution) @0x0028b0e7 (caller id=308,

1191.08 (15.52%)

13 20 Isomn target (kernel execution) @0x0027ef85

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1191.08

74.87 (6.29%)

0.00 MPI Rank 10 0.00 MPI Rank 11

0.00 MPI Rank 12

0.00 MPI Rank 13

0.00 MPI Rank 14

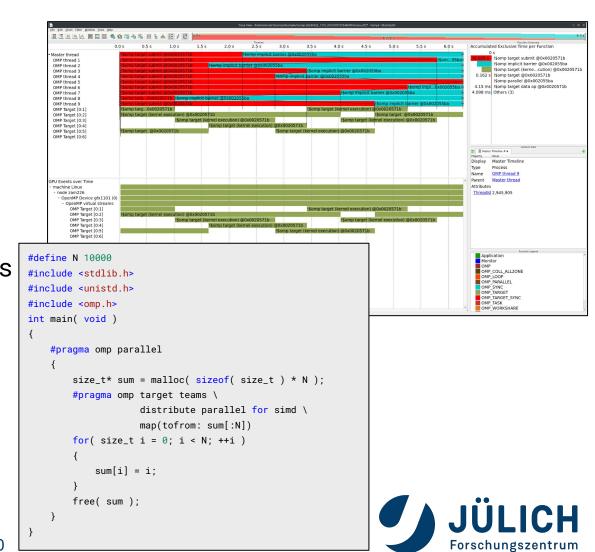
All (48 elements)

7673.64 0.00

any information about the low-level stream

for any event

- To handle overlapping events, we create OpenMP virtual streams
- May lead to more OpenMP virtual streams than actually created by runtime



LIMITATIONS

The OpenMP Tools Interface only offers so much... (as of spec. 5.2)

• The device tracing interface doesn't give us



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LIMITATIONS

Compiler and runtime limitations



- The device tracing interface doesn't give us any information about the low-level stream for any event
 - To handle overlapping events, we create OpenMP virtual streams
 - May lead to more OpenMP virtual streams than actually created by runtime
- OpenMP runtimes still have runtime issues
 - We generally recommend the latest releases, as they are the most stable

- For best support, it is advised to use the latest compiler versions to ensure best support of the device tracing interface. In our testing, ROCm 6.1.0 and AOMP 19.0-0 offer the best support, with the following limitations:
 - AOMP 19.0-0 will report incorrect times for data transfers between devices.
 - AOMP 18.0-1 may dead lock for short programs when multiple accelerators are initialized.
 - ROCm 6.1.0 and earlier and AOMP 18.0-0 and earlier do not support multiple devices per rank. If kernels are executed on more than one device per process, execution may abort. Otherwise events may be associated with the wrong accelerator.
 - AOMP 18.0-0 incorrectly maps identifiers between callbacks and the device tracing interface. This leads to data transfers being shown incorrectly between the host threads and devices.
 - ROCm 5.7.1 and earlier and AOMP 17.0-3 and earlier do not support accessing a device from multiple threads. This may lead to issues where events are associated with the incorrect host thread.
 - ROCm 5.6 to 5.7.1 do not dispatch all callbacks for `#pragma omp target enter/exit data`. Score-P will abort due to timestamp issues.
 - When utilizing multiple accelerators with ROCm 5.5, execution will dead lock at the end of the program execution when Score-P calls `stop_trace` for the device tracing interface.
 - ROCm 5.4 and earlier are not supported due to not having a way to translate the device time to host time.

Snippet from our OPEN_ISSUES for AMD (others to follow)



WHAT ABOUT OTHER RUNTIMES?

Not all runtimes do support device tracing

- Score-P will output a warning, reminding that no accelerator data will be collected by OMPT
- However, host callbacks are still recorded!

Solution: Use native GPU adapters, if possible!

\$ scorep --thread=omp:ompt -cuda nvc -mp=gpu \
 my-code.c -o my-code.out
\$ SCOREP_ENABLE_TRACING=true ./my-code.out

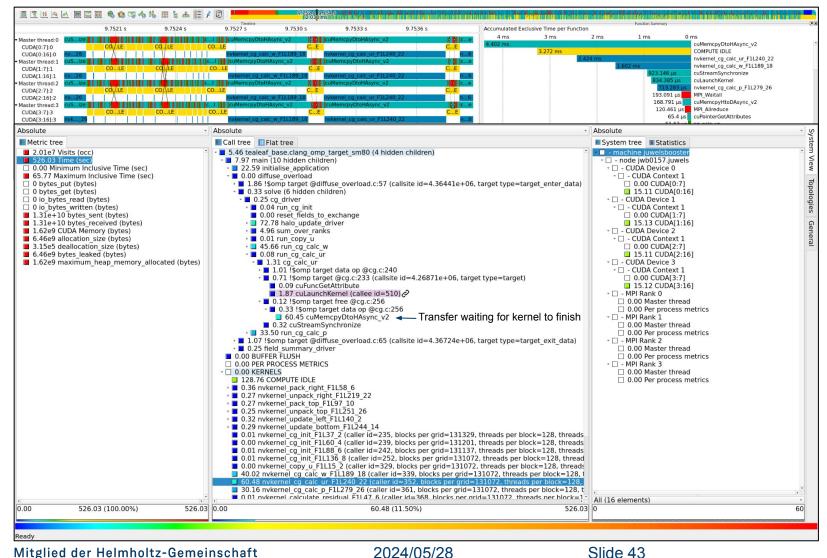


Compiler	Host	Events	Accelerator Events
	Host	Target	Device Tracing
AOMP 18.0-1	Full	Full	Full
CCE 17.0.0	None	Full	None
Clang 18.1.6	Full	Partial	None
GCC 14.1	None	None	None
NVHPC 24.5	Full	Full	None
oneAPI 2024.1	Full	Partial	None
ROCm 6.1	Full	Full	Partial



COMBINING OMPT AND CUPTI

Showing off results on JUWELS Booster with NVHPC 23.7 on one node with 4x A100



• OMPT adapter is still able to record host events

Score-P

Scalable performance measurer infrastructure for parallel codes

- Some host events might show longer times than expected
 - Synchronization
 points of low-level
 runtime
- Native accelerator adapter records kernels and data transfers



FINAL WORDS

Scalable performance measurement infrastructure for parallel codes

A short overview of what was shown in this talk

- With Score-P v9.0, we will expand our OpenMP support in several ways
- Most important: Users will be able to record OpenMP target events
 - AMD compilers sufficiently support the OpenMP Tools Interface
 - For other compilers: Native accelerator adapters required to get events
- Some compromises had to be made, partially because of the 5.2 specification
- Available implementation already works on several different systems and on small and large scale



OBTAIN SCORE-P AND GET IN CONTACT



• Visit our web page:

https://score-p.org

• Check out our public GitLab mirror:

https://gitlab.com/score-p/scorep

• Available on several different platforms:





https://go.fzj.de/scorep-ompt-device-tracing Get the Score-P development version



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THANKS FOR YOUR ATTENTION! QUESTIONS?



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